PRELIMINARY AMENDMENT
Serial Number: 10/698333
Filing Date: October 31, 2003
Title: PORTABLE HEARING-RELATED ANALYSIS SYSTEM

Page 2
Docket No: 1346.009US3

IN THE SPECIFICATION

Please amend the paragraph beginning on page 50, line 8 as follows:

FIG. 13 is a block diagram of PCMCIA real-ear Card. The real-ear Card has a jack 500 for plugging into a PCMCIA slot in a host computer, and provides VCC power on line 502 to the Card. PCMCIA interface 504, with the CIS and the host interface, provide card identifying information to the host computer via line 506 and receive signals from the host computer via line 506. A microprocessor ~~508~~ 509 is coupled to reset circuit 510 and to oscillator 512 as described above. A memory system 514 communicates via line 516 with the interconnection 518 between interface 504 and microprocessor ~~508~~ 509.

Please amend the paragraph beginning on page 50, line 19 as follows:

The ~~microprocesor 508~~ microprocessor 509 controls the functioning of the real-ear analyzer system and provides control signals on line 520 to the audio control section 522, and on line 524 to the tone control section 526. The audio control section 522 includes noise generator circuits 528, and the tone control section 526 includes controlled oscillator circuits 530.

Please amend the paragraph beginning on page 51, line 6 as follows:

The stimulus control section 534 is controlled by microprocessor ~~508~~ 509 with signals received on line 548, to select the appropriate input signals. The input signals are provided on line 550 from the narrow-band signal source 534, on line 552 from the speech spectrum noise signal source 538, on line 554 from the pure tone signal source 542, and on line 556 from the FM pure tone signal source 546. The stimulus selector control 534 selectively provides output signals on line 558 to the attenuator circuits 560, on line 562 to the interruptor circuits 564, or on line 566 to the pulser circuit 568.

PRELIMINARY AMENDMENT
Serial Number: 10/698333
Filing Date: October 31, 2003
Title: PORTABLE HEARING-RELATED ANALYSIS SYSTEM

Page 3
Docket No: 1346.009US3

Please amend the paragraph beginning on page 51, line 17 as follows:

The microprocessor ~~508~~ 509 provides control signals on line 570 to the output control circuits 572 for appropriately selecting the stimulus signal to be applied to speaker system 502. Attenuator circuits 560 provide signals on line 574, interruptor circuits 564 provide signals on line 576, and pulser circuit 568 provides signals on line 578 to the output control circuit 572.

Please amend the paragraph on page 51, line 24 as follows:

In response to the various selectable stimulus signals, probe microphone 508 provides sensed real-ear response signals on line 510. These signals are provided on line 580 to the real-ear response circuits 582. The real-ear response circuits 582 operate under control of control signals provided on line 584 from the microprocessor ~~508~~ 509, to feed back selected signals via line 586 to the microprocessor. The microprocessor ~~508~~ 509 either directly transmits the real-ear response parameters through the interface 504 to the host computer, or temporarily stores the responses in the memory system 514 for later uploading to the host computer.

Please amend the paragraph on page 52, line 18 as follows:

A printer control section 590 receives control signals from microprocessor ~~508~~ 509 on line 592, and receives real-ear response data signals on line 594. The parameters to be recorded are provided on line 596, and thence to line 552 for printing by printer 550.

PRELIMINARY AMENDMENT
Serial Number: 10/698333
Filing Date: October 31, 2003
Title: PORTABLE HEARING-RELATED ANALYSIS SYSTEM

Page 4
Docket No: 1346.009US3

IN THE DRAWINGS

Figure 13 is amended to renumber microprocessor 508 as microprocessor 509. A red-lined copy and a formal copy of Figure 13 are included herewith.